

VLSI Design Lab

Co-Ordinator: Dr. V Sridhar

In view of Industrial requirements and large demand for VLSI engineers around the world, the subjects VLSI design and embedded systems are included as core subjects in the curriculums of Electronic and communication engineering, Electronic instrumentation engineering and Medical electronics branches of Bachelor of Engineering program.

The major objectives of VLSI design lab are:

- To provide good VLSI design facility to UG and PG students and develop a facilities to research scholars in the field of VLSI design for achieving excellence in this field.
- To motivate faculty members to take up research work and guide students in this specialize area.
- To conduct regular training program for students and faculty members from other colleges and poly-techniques who wants to improve their knowledge and practical skill in VLSI design and embedded system.

The VLSI design lab created at PESCE Mandya using KFIST-I VGST grant has been effectively used and is one of the best lab facilities available to train VLSI engineers.

The concept and objective of VLSI Design center:

- Promoting VLSI research and education leading to product development and commercialization.
- To create awareness about VLSI technology among the students/researchers as a potential field of research and entrepreneurship.
- To create specialized VLSI design skill among the students.
- To provide specialized training and services to existing VLSI design center.
- To promote research and project collaboration between PESCE, Mandya and existing VLSI design and embedded system organizations.
- Aiding existing companies through access to the expensive EDA tools, and infrastructure available with the center.
- To encourage VLSI design house for setting up their industries in Mandya district by providing infrastructural supports with nominal charge from this incubation centre.
- To help in chip design programme.